

RESUME

Pramod V Rampur M.Tech, MISTE. (UGC Approved)

Assistant Professor,
Department of Electronics and Communication Engineering,
KLEIT, Hubballi, PIN-580027
Email: pramod_rampur@yahoo.co.in



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ACADEMIC CREDENTIALS:

- 1. Ph.D on Antennas (Open Seminar-1 is completed and Open seminar-2 is scheduled on 27-12-2024),** pursuing at PDA College of Engineering, Kalaburagi, registered in 2019
- 2. M.Tech (VLSI & ESD),** KLE Engineering College, Belgaum, 2011 with aggregate **73%** (1st and 4th SEM).
- 3. BE (Electronics & communication Engg),** BLDE's College of Engineering, Bijapur (Vijayapur), 2009 with aggregate **71%** (1st to 8th SEM).
- 4. PUC,** R D Patil College of science, Sindgi, 2005 with **84%**.
- 5. SSLC,** H G High School, Sindgi, 2003 with **88%**.

PROFESSIONAL EXPERIENCE:

Nature of job	Employer's name and address	Duration	Designation
Teaching	KLEIT, Hubballi	2023 to till date	Assistant Professor
Teaching	PESITM, Shovamogga	2015 to 2023	Assistant Professor
Teaching	SITCOE, Yadrav, Ichalakaranji (Maharashtra)	2012 to 2015	Assistant Professor
Teaching	CIT, Gubbi, Tumkur	2011 to 2012	Lecturer

AREA OF TEACHING:

1. Subjects Taught:

Microwave and Antennas, ARM Microcontroller and Embedded System, Microwaves and Radar, VHDL, Electronic System Design, Embedded System Design, Antennas and Propagation, Digital Switching System, Real Time Systems, Digital Systems and Finite Automata Theory.

2. Areas of Interest:

Antennas and Wave propagation, Microwave Engineering, Embedded Systems, VLSI Design.

3. Laboratories Handled:

VHDL Laboratory Advanced Communication Laboratory, Analog Communication Laboratory, DSP Laboratory, Digital Design Laboratory, Embedded Controller Laboratory.

RESPONSIBILITIES HELD AT DEPARTMENT LEVEL AND COLLEGE LEVEL:

1. IA coordinator at Department level.
2. Project and Seminar coordinator at Department level.
3. Time-Table coordinator at Department level.
4. NBA Criteria-3 coordinator at Department level.
5. NAAC Criteria-2 coordinator at Department level.
6. Media and Publicity coordinator at College Level.

WORKSHOPS ATTENDED:

1. Two day's workshop on "**Systematic Transformation in Engineering Education**", ICTIEE 2024, at KLE Technological University, Hubballi.
2. SPARC Sponsored One Week Workshop on "**Metamaterial Antenna Design and Deep Learning Techniques for Modern Wireless Communication**" organized by the Department of ECE, NIT, Tiruchinapalli, Tamil Nadu.
3. AICTE Share and Mentor Margadarshana Scheme Guest Lecture Series on "**Smart Antennas and Electromagnetics**" organized by JNNCS, Shivamogga.
4. One-week national level FDP on **ARM Microcontroller and Embedded System using ARM Cortex M3** conducted by JNNCE Shivamogga.
5. Workshop on **E-YANTRA** Organized by DKTE – Ichalakaranji.
6. Two-week ISTE workshop on **Analog Electronics** conducted by IIT Kharagpur.
7. Workshop on **Akash for education** Organized by IIT, Bombay.

FACULTY DEVELOPMENT PROGRAMS ATTENDED:

1. FDP on “Design and Implementation of Advanced VLSI Architecture”, by **LBCE, Andra Pradesh** on **13th March 2023**.
2. Webinar on “**Challenges in Image Quality Assessment Techniques**”, at PESITM, Shivamogga.
3. Webinar on “**Antenna Design**”, at PESITM, Shivamogga.
4. As coordinator for FDP on “**Research Opportunities in Modern Antenna Design**”, held at PESITM, Shivamogga.
5. Two days webinar on “**Accelerated Design Approach for Antenna and Antenna Arrays using Altero Feko** ” held at BIT, Bengaluru.
6. Three days FDP on “**Data Science with Machine Learning and Python using Jupyter Notebook**” at PESITM, Shivamogga.
7. Three days FDP on “**Frontiers in Advanced Materials, Manufacturing and Sustainability**” at PESITM, Shivamogga.
8. Certification of appreciation for contribution in “**5th Industry Institute Conclave on Electronics, Computational and Communication Technologies**” at PESITM, Shivamogga.
9. VTU TEQUIP 1.3, Sponsored FDP on “**Research Funding Opportunities and Proposal Preparation for Academicians**” at PESITM, Shivamogga.
10. FDP on **ARM Microcontroller & Embedded System Using ARM Cortex M3**, at JNNCE, Shivamogga.
11. Four days FDP at PESITM Shivamogga.

PAPER PUBLICATIONS IN INTERNATIONAL JOURNALS:

1. Pramod V Rampur, Ramachandra Turkani, “Design and Performance Analysis of Frequency Reconfigurable Monopole Antenna” in **IJDRD**, Vol.11, Issue9, 2023.
2. Pramod V Rampur, Jagadish M and Dr. Geeta Hanji, “Design of Reconfigurable Antenna for wireless Applications” in **IRJMETS**, Volume 04, Issue 09, September 2022.
3. Pramod V Rampur, Dr. Geeta Hanji, “A Novel Metamaterial based on Concentric Circular Split Ring Mounted Epoxy Substrate for Negative Permittivity and Permiability” in **IJME** Volume 07, May 2022.
4. Pramod V Rampur, Dr. Geeta Hanji, “Design of Compact Monopole Antenna for Wireless Communication Applications”, in **IJSRD**, Volume 09, Issue 05, July 2021.
5. Pramod V Rampur, “Design of 1X2 Microstrip Array With High Isolation”, in **IRJMETS**, Volume 03, Issue 07, July 2021.
6. Pramod V Rampur, “Isolation Enhancement of Patch Antenna Array for LTE Applications”, in **IRJMETS**, Volume 02, Issue 08, August 2022.
7. Pramod V Rampur, “Intelligent Vehicle Monitoring Using ARM Microcontroller System

based on GSM and GPS Technology”, in **IJSART**, Volume 06, Issue 08, August 2022.

8. Pramod V Rampur, Dr. Geeta Hanji, “Mutual Coupling: Concepts, Challenges and Research Areas”, in **IJSRD**, Volume 08, Issue 05, July 2020.
9. Pramod V Rampur, Jagadish M, Yogeesh G, Erroneous Electric Field Intensity Analysis in the Near-Field regime due to Dipole and Waveguide As Measuring Probes, Volume 3, Issue 11, In Jan 2016. (**IJSRD**).
10. Pramod V Rampur, Jagadish M, Yogeesh G, Design and Implementation of Advanced Array Multiplier for Binary Multiplication on FPGA, In **IJERT**, Volume 5, Issue 8, in August 2016.

PAPER PUBLICATIONS IN NATIONAL CONFERENCES:

1. Pramod V Rampur, Present Education in India: Reflections on the Critical Issues and Concerns held at PESIAMS, Shivamogga, on 15th Nov 2019.
2. Pramod V Rampur, Design and Performance Analysis of Multiband Microstrip Patch Antenna held at PESITM, Shivamogga, on 30th April 2019.
3. Pramod V Rampur, Rakesh Wali, Dr. Geeta Hanji, Design, Fabrication and Testing of Rectangular Microstrip Patch Antenna Array with Increased Gain For S-Band Application, in the National Conference on Research in Science, Engineering and Management held at PESITM, Shivamogga, on 26th May 2018.
4. Pramod V Rampur, Design and Implementation of Low Power CMOS 10:4 Compressor for Multipliers in 180nm, in the National Conference on Research in Science, Engineering and Management held at PESITM, Shivamogga, on 20th May 2017.
5. Pramod V Rampur, Design, Implementation and Comparison of Wallace Tree Multiplier Using Conventional Full Adder and Full Adder with Alternative Scheme, in the National Conference on Research in Science, Engineering and Management held at PESITM, Shivamogga, on 20th May 2017.
6. Pramod V Rampur, Design and Implementation of Advanced 8X8 Multiplier for Binary Multiplication Using Different Compressors on FPGA, in the National Conference on Research in Science, Engineering and Management held at PESITM, Shivamogga, on 20th May 2017.

AWARDS:

1. Approved by University Grant Commission.
2. Life membership of ISTE. (MISTE).
3. Projects guided for UG: 14, for PG :1

PROJECT WORK UNDERTAKEN:

1. M.Tech (VLSI & ESD): FPGA design and implementation of digital multiplier less PID controller.
2. BE (E&C): Vehicle automation cum black box.

STRENGTHS:

1. Good verbal and written communication skills.
2. Ability to learn technology practices.
3. Good team player and team facilitator.

HOBBIES:

1. Travelling.
2. Playing Chess.
3. Playing Cricket

PERSONAL DETAILS:

Gender & Marital Status : Male & Married
DOB : 22nd Jul 1987
Languages Known : English, Hindi, and Kannada.
Passport number : not Available
Permanent Address : S/o V M Rampur
GayatriKrupa, KalikaNagar
Sindgi-586128, Dist-Bijapur
Phone No-(08488)221722

Date:

Place:

(Pramod.V. Rampur)