Akshata S Radder (Lavanya V Gadagi)
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OBJECTIVE:

To get involved with an ambitious and progressive organization that gives me the freedom and encouragement to vent all my potentials and skills and dynamically work towards the growth of organization and society.

SUMMARY OF QUALIFICATIONS:

- Crystal clear understanding of the underlying principles of the subject and its relevancy to other domains
- Perfect knowledge about the common job duties of a lecturer and ability to perform them efficiently
- A little familiarity with the general administrative environment at educational institutes and idea about their practice
- Ability to provide a training program to the undergraduate students as well as motivating them for achieving the desired effects
- Ability to grasp new concepts in less time

EXPERIENCE:

Worked as Assistant Professor in Electrical and Electronics Department, AMC Engineering College, Bangalore from the year August 2013-June 2017.

SUBJECTS and LABS HANDLED:

- Basic Electrical Engineering
- Operational Amplifiers and Linear ICs and its associated Laboratory
- Digital Signal Processing and its associated Laboratory
- Control System and its associated Laboratory
- Digital system design and its associated Laboratory
- Power Electronics and its associated Laboratory

ACADEMIC EDUCATION:

No	Qualification	Specialization	College	Aggregate	Passing Year
1	M.Tech	Microelectronics and Control System	Dayanand Sagar College of Engineering, Bangalore	70.00%	2010-2012
2	B.E	Electrical and Electronics	Nitte Meenakshi Institute of Technology, Bangalore	70.00%	2006-2010
3	P.U.C	PCMC	S.J.V.V.S College, Gadag	77.83%	2004-2006
4	S.S.L.C		Basaveshwara High School, Gadag	82.03%	2003-2204

INTERESTS AND HOBBIES:

- Reading short stories
- Playing chess, carrom and tennikoit
- Reading science fiction books and novels
- Listening to Music

TECHNICAL SKILLS:

- Programming Languages : C, Matlab, VHDL,Verilog
- Assembly Level Languages : Microprocessor 8086, Microcontroller 8051

PERSONAL SKILLS

- Effective Presentation skills
- Ability to adjust to the situation
- Taking knowledge from others.

PERSONAL DETAILS:

Husband Name	:	Vinaykumar J Gadagi
Date of Birth	:	December 19th, 1988
Gender	:	Female
Marital Status	:	Married
Nationality	:	Indian
Languages Known	:	English, Kannada, Hindi and Telegu

PROJECT WORK IN B.E

Organization: Structural Integrity Division (SID), National Aerospace Laboratory (NAL), Bangalore

Title: **Microcontroller based Fatigue test controller for drop tank test**

Description: In this new era of immense science and technological advancements the older and less reliable systems are replaced by electronically controlled systems which are much more reliable and efficient. Microcontroller is one such advancement that the new electronics age has brought about.

The main aim of project is to design a microcontroller-based fatigue test controller for a drop tank test with the display unit which is ideally suited for aircrafts particularly in military services. This helps us to reduce the complexity, space required so that speed can be increased.

Presently aircrafts are equipped with drop tanks to carry extra fuel for long range flights. The fatigue test is conducted on the bases of motorized unit manually. In this project microcontroller in used to give a set of conditions are defined and programmed for pumping.

PROJECT WORK IN M.Tech

Title: **FPGA Implementation of radix-4 FFT using CORDIC Algorithm**

Description: In today's technological world trigonometric functions sine and cosine values has found numerous applications in various areas. These have found applications in various place such as Navigation systems, Digital signal processing, Robot control, Software Defined Radio, Math processors etc. Normally these values are calculated using software.

CORDIC is an iterative arithmetic computing algorithm capable of evaluating various elementary functions using unified shift-and-add operations. Fast Fourier Transform processor based on CORDIC is implemented. The key ideas are replacing the sine and cosine twiddle factors in conventional FFT architecture by iterative CORDIC rotations which allow the reduction in read-only memory (ROM). The use of CORDIC in FFT results in the elimination of multipliers, saves area, power and cost CORDIC finds many applications as it providing a simpler way of computing complex multiplications. It is proved that CORDIC is most suitable alternative.

CORDIC based FFT is implemented on FPGA Spartan-3 and language used is VHDL.

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EXTRACURRICULAR ACTIVITIES:

- Presented paper in International conference on Current trends and Management.
- Won the Second prize in 'Circuit debugging' held in NMAIT, Mangalore in 22nd CSI Karnataka student convention.
- Attended the course on 'VLSI FPGA DESGIN' from 18-09-2011 to 27-09-2011 at VED Labs.
- Participated in Faculty Development Program on "Creative Thinking on Intellectual Property Generation" held on 1st March 2014 at AMC Engineering college, Bangalore.
- Volunteered for On-spot Programming in Anaadyanta, Inter Collegiate annual techno-cultural festival organized in NITTE Meenakshi Institute of Technology, 2009.
- 1st in NHA, NITTE Hostelite Association, Inter Hostel Cultural event in 2006 and 2007.

DECLARATION:

I hereby declare that all the statements mentioned above are true, complete and correct to the best of my knowledge and belief.

Date: 20-02-2023

Place: Hubli Akshata S Radder