

CURRICULUM VITAE

Prashant D.Hanasi

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OBJECTIVE

Aspiring to work in challenging environment and add the value to the organization that I serve by upgrading my skills and knowledge. To be in a sector where work seems where there is scope for learning new things always.

EDUCATIONAL QUALIFICATIONS

Qualification	Institution	Board/University	Percentage
Ph.D. (2021)	Basaveshwar Engineering College (Autonomous), Bagalkot (Research center)	Visvesvaraya Technological University, Belgavi	Pursuing (Coursework completed)
M.Tech (Digital Communicaton) (2015)	Basaveshwar Engineering College (Autonomous), Bagalkot	Basaveshwar Engineering College (Autonomous), Bagalkot	CGPA:9.74 (89.8%)
B.E (E&CE) (2012)	Basaveshwar Engineering College (Autonomous), Bagalkot	Visvesvaraya Technological University, Belagavi	CGPA:8.94 (81.90%)
Pre-University (2008)	JSS PU Science college Dharwad	Department of Pre-University Education, Bengaluru	78.66
Secondary school Education (2006)	SMTN Govt High School, Toranagatti	Karnataka Secondary Education Examination Board Bengaluru	87.68

TECHNICAL SKILLS

Programming language : C, Python, Verilog.

Tools : Cadence, Comsol multiphysics, HFSS, Matlab, Scilab, Labview and Coventorware.

Subject of interest : Control systems, Logic Design, MEMS, Network theory, Analaog and digital circuits, communication systems and Antennas.

TEACHING EXPERIENCE

- Worked as Teaching assistant under TEQIP Phase-2 program during M.Tech at BEC Bagalkot (August 2013 to June 2015).
- Worked as Assistant Professor in Dept. of Electronics and Communication Engineering at Jain College of Engineering, Belagavi (June, 2015 to February 2021).
- Working as Assistant Professor in Dept. of Electronics and Communication Engineering in KLE Institute of Technology, Hubballi (March, 2021 – till date).

PROJECTS UNDERTAKEN

- Novel Micro switch And Micro relays(**UG Academic project**)
- Study of Pull-in Voltage in MEMS Actuator (**M.Tech Mini project**)
- Novel comb-drive actuators for efficient electrostatic switching(**M.Tech Academic project**).

ACHIEVEMENT

- Secured 1st rank in M.Tech 3rd semester as academic topper.

PAPER PUBLISHED

- **Prashant D.Hanasi**, B.G. Sheeparamatti, Kirankumar B. B. “Study of Pull-in Voltage in MEMS Actuator” selected for **International Conference on Smart Structures & Systems (ICSSS-2014), Chennai-INDIA.**
- B G Sheeparamatti, **Prasahnt D. Hanasi**, M. N. Patil“Simulation of Electrostatic Microactuator with TwoSided Electrodes”**National Symposium on Microsystems Technologies(NSMT-2015)**,NITK Surathkal.Journal of Mechatronics and Automation Volume 2, Issue 1.
- B G Sheeparamatti, **Prasahnt D. Hanasi**, VanitaAbbigeri“Critical Analysis of Different Actuation Techniques for a Micro Cantilever” is accepted for, **17th International Conference on Microelectronics, Nanoelectronics and Photonics (ICMNP 2015)** to be held in **London, United Kingdom** during November, 27-28, 2015.
- Paper titled “Study of Capacitance in Electrostatic Comb-Drive Actuators” selected for **COMSOL conference October 29-30, 2015, Pune.**

PAPER PRESENTED

- Presented a paper on “MEMS sensor modeling and simulation using COMSOL Multiphysics tool” at Dept. of E&CE, **BVCET Hubballi.**
- Presented a paper on “Simulation of Electrostatic Micro actuator with TwoSided Electrodes” at **NITK Surathkal.**
- Paper titled “Novel Comb-Drive Actuator for Efficient Electrostatic Switching” presented at **IISC Bangalore researcher’s forum-2019.**

CONFERENCE ATTENDED

- COMSOL Conference 2014 held on November 13-14, Bangalore.
- National Symposium on Microsystems Technologies (NSMT-2015), held on January 9th & 10th 2015, at NITK Surathkal.
- COMSOL Conference 2015 held on October 29-30, at Pune.
- Attended CONIT 2021, 2023 and 2024 hosted by KLEIT Hubballi.

FDP CONDUCTED

- Conducted the faculty development program on modeling and simulation of MEMS, at Dept. of E&CE, **BVBCET Hubballi** as **resource person**.
- Conducted the faculty development program on modeling and simulation of MEMS, at Dept. of E&CE, **AIET Moodabidri** as **resource person**.

FDP ATTENDED

- Attended five days FDP on "LabVIEW programming" at KLS GIT Belagavi.
- Attended one day FDP on e-Sim at IIT Bombay.
- Completed 8 week NPTEL course on "digital system design using verilog".
- Attended five days FDP on "Trends In MemS & Nanotechnology" organized by AIET Moodabidri.
- Attended five days FDP on "Sensor based System Design using Graphical Interface" organized by KLS GIT Belagavi.
- Attended 3 days online FDP on "Research grants, Publications and Consultancy skills for academics" organized by BIT Bengaluru.
- Attended five days FDP on "Artificial intelligence for edge computing" organized by KLS GIT Belagavi.

COURSES COMPLETED

- Completed digital system design using verilog 8 weeks NPTEL course.
- Completed Arduino programming, Python for beginners, C programming for everybody, Machine learning courses in Coursera.
- "Industrial training on IoT & Machine learning using Labview" by VI solutions Bangalore.
- "Industrial certification on graphical programming using Labview" by VI solutions Bangalore.
- Introduction to Programming with MATLAB from Coursera platform.
- The complete LabView programming beginner to advanced from udeMy platform.
- Completed course on VLSI for beginners organized by NIELIT Calicut.
- Completed course on python for beginners organized by NIELIT Calicut.

PERSONAL DETAILS

Name : Prashant D.Hanasi
Date of Birth : 8th July 1990
Gender : Male
Language Known : English, Kannada and Hindi
Permanent Address :Prashant D. Hanasi
A/P: Toranagatti, TQ: Ramadurg
Dt: Belgavi 591114

DECLARATION

I hereby declare that the above-furnished details are true to the best of my knowledge.

Place: Belgavi

Prashant D.Hanasi